GF55: ICC

Libraries

Name	Process	Form Factor
RGO_GF55_25V33_LPX_30C_ICC	LPX	staggered

Summary

The ICC library provides the bidirectional reset, clock, and data I/O drivers for the smart card UICC terminal interface. This library has been designed for and is compliant with Class A, Class B, and Class C operation as defined in the ETSI TS 102 221 technical specification – Smart Cards; UICC-Terminal interface; Physical and logical characteristics (Release 11).

- Bi-directional smart card UICC terminal drivers
  - Class A – 5V operation
  - Class B – 3.0V operation
  - Class C – 1.8V operation
- Full complement of power, corner, and spacer cells

ESD Protection

I/O pads are designed with robust ESD protection for all market segments. Passed:

- 2KV ESD Human Body Model (HBM)
- 200 V ESD Machine Model (MM)
- 500 V ESD Charge Device Model (CDM)

Recommended operating conditions

	Description	Min	Nom	Max	Units
V_{VDD}	Core supply voltage	0.81	0.9	0.99	V
		0.90	1.0	1.10	V
		1.08	1.2	1.32	V
V_{DVDD}	I/O supply voltage	4.50	5.0	5.50	V
		2.70	3.0	3.30	V
		1.62	1.8	1.98	V
T_J	Junction temperature	-40	25	125	°C
V_{PAD}	Voltage at PAD	-0.3	-	V_{DVDD} +0.3	V

Characterization Corners

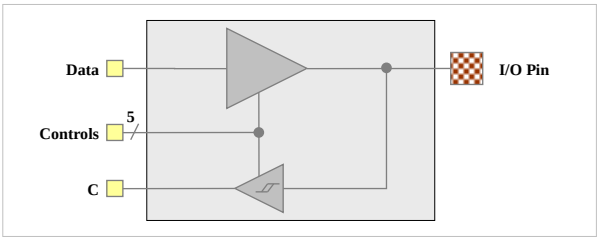
Nominal VDD	Model	VDD	DVDD[1]	Temperature
0.9 / 1.0 / 1.2	FF	+10%	+10%	-40°C
	FFF [1]	+10%	+10%	125°C
	FFF [1]	+10%	+10%	150°C
	TT	nominal	nominal	25°C
	SS	-10%	-10%	-40°C
	SS	-10%	-10%	125°C
	SS	-10%	-10%	150°C

[1] The FFF requirement is for leakage only. Aragio cannot guarantee that the AC/DC specifications will be met for the FFF model corner

SCP_BI_RST_5V_NC

Description

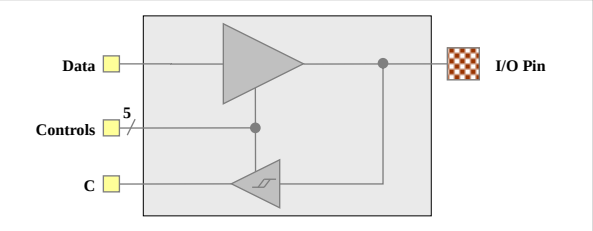
Bi-directional UICC Reset terminal driver



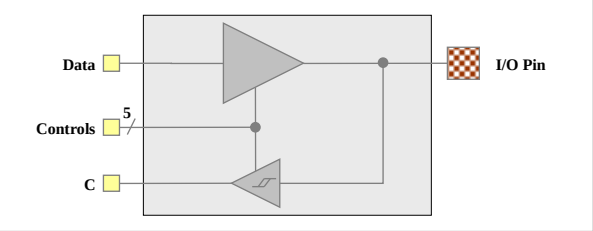
SCP_BI_CLK_5V_NC

Description

Bi-directional UICC Clock terminal driver



SCP_BI_DIO_5V_NC



Description

Bi-directional UICC Data terminal driver

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