TSMC22: 3.3V 32kHz Oscillators



Libraries

Name	Process	Form Factor
RGO TSMC22 25V33 ULL 40C OSC 032	ULL	Inline CUP

Summary

The 3.3V 32kHz Oscillators library provides oscillator macro cells designed to generate an asynchronous on-chip clock signal with an appropriate external oscillator crystal.

32 kHz Real Time Clock Oscillator

This library is available in an inline CUP wire bond implementation The CUP structures are built into the I/O cells.

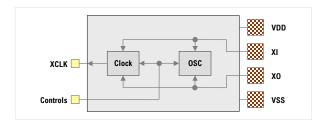
ESD Protection:

- JEDEC compliant
 - o 2kV ESD Human Body Model (HBM)
 - o 500V ESD Charge Device Model (CDM)

Latch-up Immunity:

- JEDEC compliant
 - o Tested to I-Test criteria of ± 100mA @ 125°C

OSP_BI_032_11V



32 KHz RTC Oscillator Features

- Designed to use a 32.768 kHz external crystal
- Optimized for stability, minimum jitter & low power (3μW)
- Characterized with crystal loading capacitors ranging from 7 pF to 40 pF.
- Power-down mode
- Bypass mode
- Speed-up circuitry for fast startup
- Operates on core power only (VDD/VSS cells embedded)

Cell Size & Form Factor

• Inline (core-limited) – 120μm x 280μm

Characterization Corners

Nom VDD	Model	LPE	VDD	DVDD [1]	Temp
0.8V / 0.9V	FF	Cbest	+10%	+10%	-40°C
	FF	Cbest	+10%	+10%	0°C
	FF	Cbest	+10%	+10%	125°C
	FFG	Ctypical	+10%	+10%	125°C
	TT	Ctypical	nominal	nominal	25°C
	TT	Ctypical	nominal	nominal	85°C
	SS	Cworst	-10%	-10%	-40°C
	SS	Cworst	-10%	-10%	0°C
	SS	Cworst	-10%	-10%	125°C

[1] DVDD = 1.8V, 2.5V, 3.3V

Recommended Operating Conditions

	Description	Min	Nom	Max	Units
V_{VDD}	Core supply voltage	0.72	0.8	0.88	V
		0.81	0.9	0.99	V
V_{DVDD}	I/O supply voltage	1.62	1.8	1.98	V
		2.25	2.5	2.75	V
		2.97	3.3	3.63	V
TJ	Junction temperature	-40	25	125	°C
V_{PAD}	Voltage at XI [1]	0	-	V_{VDD}	V

[1] XI can be driven by an external clock.

XO should never be driven or loaded by anything other than the crystal,

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